



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

TFW/S

Applicant: Leonard Forbes et al.

STRAINED SEMICONDUCTOR BY FULL WAFER BONDING

Docket No.: 1303.109US1

Serial No.: 10/623,788

Filed: July 21, 2003

Due Date: December 18, 2006

Examiner: Pamela E. Perkins

Group Art Unit: 2822

MS Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

- ☒ Return postcard.
- ☒ Amendment and Response (17 pgs.).
- ☒ Communication Concerning Related Applications (2 pgs.).
- ☒ Supplemental Information Disclosure Statement (2 pgs.), Form 1449 (1 pg.).
- ☒ Authorization to charge Deposit Account 19-0743 in the amount of \$180.00 to cover the fee for consideration of Information Disclosure Statement under 97(c).

If not provided for in a separate paper filed herewith, Please consider this a **PETITION FOR EXTENSION OF TIME** for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Customer Number 21186

By: Marvin L. Beekman

Atty: Marvin L. Beekman

Reg. No. 38,377

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 18 day of December, 2006.

Lisa Rosaske

Name

Lisa Rosaske

Signature

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

(GENERAL)



S/AB 10/623,788

PATENT

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COMMUNICATION CONCERNING RELATED APPLICATIONS

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Applicants would like to bring to the Examiner's attention the following related applications in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date/Issue Date</u>	<u>Attorney Docket</u>	<u>Title</u>
11497632	August 2, 2006	1303.186US1	STRAINED SEMICONDUCTOR, DEVICES AND SYSTEMS AND METHODS OF FORMATION
11475798	June 27, 2006	1303.109US2	STRAINED SEMICONDUCTOR BY FULL WAFER BONDING
11493128	July 26, 2006	1303.099US3	SEMICONDUCTORS BONDED ON GLASS SUBSTRATES
11460391	July 27, 2006	1303.103US2	GETTERING OF SILICON ON INSULATOR USING RELAXED SILICON GERMANIUM EPITAXIAL PROXIMITY LAYERS
11460398	July 27, 2006	1303.104US2	WAFER GETTERING USING RELAXED SILICON GERMANIUM EPITAXIAL PROXIMITY LAYERS
11494319	July 27, 2006	1303.102US3	STRAINED Si/SiGe/SOI ISLANDS AND PROCESSES OF MAKING SAME


Continuations and divisionals may be later filed on the cases listed above, or cited to the Examiner in any previous Communication Concerning Related Applications. Applicants request that the Examiner review all continuations and divisionals of the above-listed or previously-cited patent applications before allowing the claims of the present patent application.

Respectfully submitted,

LEONARD FORBES ET AL.

By Applicants' Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
P.O. Box 2938
Minneapolis, MN 55402
(612) 373-6960

Date 12-18-06 By 
Marvin L. Beekman
Reg. No. 38,377

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